

<b>Notice of References Cited</b>	Application/Control No. 09/519,187		Applicant(s)/Patent Under Reexamination ZINGER ET AL.	
	Examiner David Y Jung		Art Unit 2134	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,782,479	08-2004	Williams et al.	713/194
*	B	US-6,775,778	08-2004	Laczko et al.	713/194
*	C	US-6,771,446	08-2004	Noble, Gayle L.	713/200
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)			
	U	Ze et al., A research on system-level architecture of single-chip cryptographic data processing, Communications, Circuits and Systems and West Sino Expositions, IEEE 2002, International Conference on, Volume 2, 29 June-1 July 2002, pp. 1486-1490			
	V	McLoone et al., A single-chip IPSEC cryptographic processor, Signal Processing Systems, 2002, IEEE Workshop on, 16-18 Oct. 2002, pp. 133-138			
	W	DuPont et al., Robustness IPs for reliability and security of SoCs, Test Conference, 2002, Proceedings, Interenational, 7-10 October 2002, pp. 357-364			
	X				

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.